

IFW
AF/2823

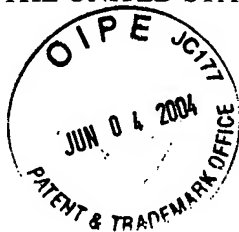
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Ji Suk HONG, et al.

Serial No.: 10/098,524

Filed: March 18, 2002



Group Art Unit: 2823

Examiner: J. Garcia

For: METHOD FOR FABRICATING SEMICONDUCTOR MEMORY DEVICE

TRANSMITTAL

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith is an Amendment Under Rule 1.116 for filing in the above-captioned patent application.


The fee has been calculated as shown below:

	Claims Remaining After Amendment	Highest Number Previously Paid For	Present Extra	Small Entity Rate Addit. Fee	(or)	Other Than A Small Entity Rate Addit. Fee
Total	8	-	20 =	0	x 09 = \$	x 18 = \$
Indep.	01	-	03 =	0	x 42 = \$	x 84 = \$
___ First Presentation of Multiple Dependent Claims				+140 = \$		+ 280 = \$
Total Additional Fee				\$		\$

XXXX If a Petition for Extension of Time is necessary and the Petition and/or the check is not enclosed, this will act as the Petition and applicant herewith petitions the Commissioner to extend the time for response and charge any fees necessary under 37 CFR 1.17 (a)(1)-(5) to Deposit Account No. 06-1358. The Commissioner is also authorized to charge payment of any other additional fees associated with this communication or credit any overpayment to Deposit Account No. 06-1358. A duplicate copy of this sheet is attached.

Respectfully submitted,

JACOBSON HOLMAN PLLC

By: 
Yoon S. Ham
Reg. No. 45,307

400 Seventh Street, N.W.
Washington, D.C. 20004-2201
Telephone: (202)638-6666
Date: June 4, 2004
YSH:SCB
R:\SBAILEY\6-04\P67503US.TL